

	L #	Hit	S ar h Text	DB
1	L1	1246	257/723.ccls.	USP AT; US-P GPU B
2	L3	3301	(361/760,782,783,784,785,786,788,790,803).ccls.	USP AT; US-P GPU B
3	L4	626	361/777.ccls.	USP AT; US-P GPU B
4	L5	1513	174/261.ccls.	USP AT; US-P GPU B
5	L6	2589	(365/51,52,63,185.09).ccls.	USP AT; US-P GPU B
6	L7	974	326/30.ccls.	USP AT; US-P GPU B
7	L8	704	(257/664,728).ccls.	USP AT; US-P GPU B

	L #	Hit	S arch Text	DB
8	L9	9276	2 3 4 5 6 7 8	USP AT; US-P GPU B
9	L10	4299	9 and (terminat\$4 impedance\$1 match\$3)	USP AT; US-P GPU B
10	L11	0	2 not 10	USP AT; US-P GPU B
11	L2	444	1 and (terminat\$4 impedance\$1 match\$3)	USP AT; US-P GPU B
12	L12	904	257/724.ccls.	USP AT; US-P GPU B
13	L13	376	12 and (terminat\$4 impedance\$1 match\$3)	USP AT; US-P GPU B
14	L14	246	13 not 2	USP AT; US-P GPU B